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in ALL CAPITALS>



Submitted by

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B.S Computer Science

Session 20xx-20xx

Supervised by

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Department of Computer Science

University of Peshawar

# Project Approval

This is to certify that this project is approved and recommended as a partial fulfillment for the degree “B.S. Computer Science” from the Department of Computer Science, University of Peshawar.

**External Examiner**: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

**Internal Examiner**: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

**Chairman**: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

# Acknowledgement

# Abstract

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# List of Abbreviations

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# Chapter 1

Introduction

Hennessy and Patterson [1] provide an in depth view of computer hardware. This is an Example of using references.

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# References

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| [1] | D. A. Patterson and J. L. Hennessy, Computer Architecture: A Quantitative Approach, Morgan Kaufmann, 2011. |